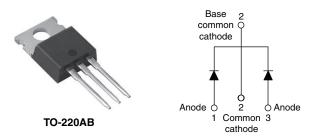


Vishay Semiconductors

Schottky Rectifier, 2 x 7.5 A



PRODUCT SUMMARY						
Package	TO-220AB					
I _{F(AV)}	2 x 7.5 A					
V _R	35 V, 40 V, 45 V					
V _F at I _F	0.51 V					
I _{RM} max.	32 mA at 125 °C					
T _J max.	150 °C					
Diode variation	Common cathode					
E _{AS}	10 mJ					

FEATURES

- 150 °C T_J operation
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
 - aced mechanical strength RoHS compliant HALOGEN
- Guard ring for enhanced ruggedness and long
 term reliability
 Auditable
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

The VS-15CTQ... center tap Schottky rectifier series has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	DL CHARACTERISTICS VALUES UNITS						
I _{F(AV)}	Rectangular waveform	15	A				
V _{RRM}	Range	35 to 45	V				
I _{FSM}	t _p = 5 μs sine	810	A				
V _F	$7.5 A_{pk}, T_J = 125 \ ^{\circ}C \ (per \ leg)$	0.51	V				
TJ	Range	- 55 to 150	°C				

VOLTAGE RATINGS									
PARAMETER	SYMBOL	VS- 15CTQ035PbF	VS- 15CTQ035-N3	VS- 15CTQ040PbF	VS- 15CTQ040-N3	VS- 15CTQ045PbF	VS- 15CTQ045-N3	UNITS	
Maximum DC reverse voltage	V _R								
Maximum working peak reverse voltage	V _{RWM}	35	35	40	40	45	45	V	

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS			
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T_{C} = 123 °C	15	А				
Maximum peak one cycle non-repetitive surge current per leg	Isou	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	810	A			
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	145				
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 1.20 A, L = 11.10 mH		10	mJ			
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zer Frequency limited by T _J maxim		1.5	А			

Revision: 25-Aug-11

1





www.vishay.com

Vishay Semiconductors

ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS		
		7.5 A	T _{.1} = 25 °C	0.55	V		
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	15 A	1j=25 0	0.70			
	V FM (*)	7.5 A	T 105 %C	0.51			
		15 A	T _J = 125 °C	0.65			
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	0.8	mA		
See fig. 2		T _J = 125 °C	$v_{\rm R} = naleu v_{\rm R}$	32			
Maximum junction capacitance per leg	CT	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 °C		400	pF		
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nH		
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs		

Note

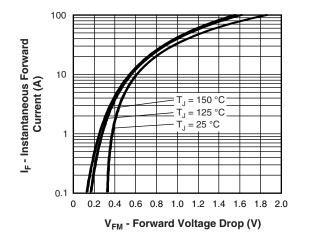
 $^{(1)}\,$ Pulse width < 300 $\mu s,\,duty\,cycle$ < 2 $\,\%$

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range	T _J , T _{Stg}		- 55 to 150	°C			
Maximum thermal resistance, junction to case per leg	P	DC operation See fig. 4	3.50				
Maximum thermal resistance, junction to case per package	— R _{thJC}	DC operation	1.75	°C/W			
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.50				
Approvimeto weight			2	g			
Approximate weight			0.07	oz.			
Mounting torque minimu	m		6 (5)	kgf ⋅ cm			
Mounting torque maximu	m		12 (10)	(lĎf ∙ in)			
			15CT	Q035			
Marking device		Case style TO-220AB	15CT	Q040			
			15CT	Q045			



VS-15CTQ...PbF Series, VS-15CTQ...-N3 Series

Vishay Semiconductors



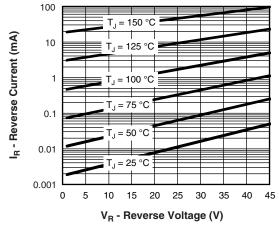


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

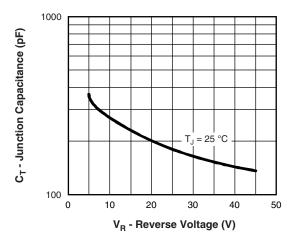


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

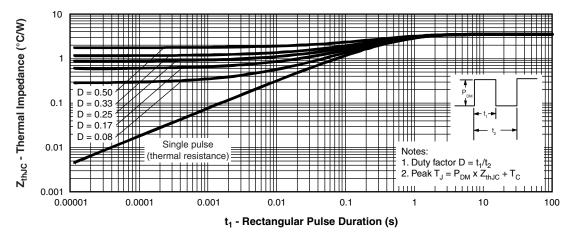
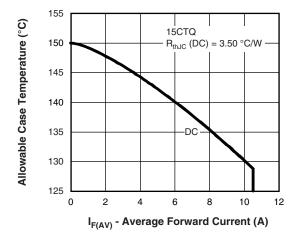


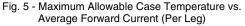
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)



VS-15CTQ...PbF Series, VS-15CTQ...-N3 Series

Vishay Semiconductors





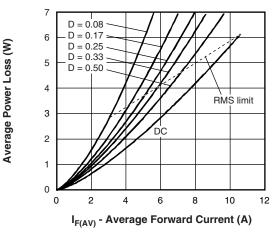


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

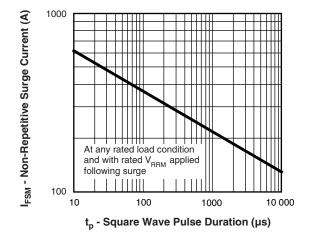


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

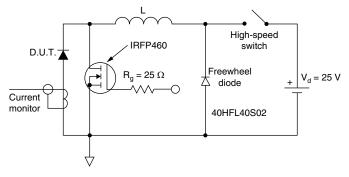


Fig. 8 - Unclamped Inductive Test Circuit

Revision: 25-Aug-11 4 Document Number: 94139 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



Vishay Semiconductors

ORDERING INFORMATION TABLE

						-	
Device code	VS-	15	с	т	Q	045	PbF
		(2)	(3)	(4)	(5)	6	(7)
	\cdot		U	\bigcirc	\bigcirc	J	\bigcirc
	1	- Vis	hay Sen	nicondu	ctors pro	oduct	
	2 - Current rating (10 = 10 A)						
	3	- Ciro	cuit conf	figuratio	n		
		C =	- Comm	on catho	ode		
	4	- Pao	ckage				
		T =	TO-220)			
	5	- Sch	nottky "C	Q" series	;		
	6	- Vol	tage rati	ing (150	= 150 \	/)	
	7 - Environmental digit						
		• [PbF = Le	ead (Pb))-free ar	nd RoHS	S complia

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-15CTQ035PbF	50	1000	Antistatic plastic tube				
VS-15CTQ035-N3	50	1000	Antistatic plastic tube				
VS-15CTQ040PbF	50	1000	Antistatic plastic tube				
VS-15CTQ040-N3	50	1000	Antistatic plastic tube				
VS-15CTQ045PbF	50	1000	Antistatic plastic tube				
VS-15CTQ045-N3	50	1000	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95222					
Part marking information	TO-220AB PbF	www.vishay.com/doc?95225			
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028			

Document Number: 94139



Vishay Semiconductors

TO-220AB

DIMENSIONS in millimeters and inches





.ead	assignments

Diodes

1. - Anode/open 2. - Cathode 3. - Anode

SYMBOL	MILLIN	IETERS	INCHES		NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- ⁽²⁾ Lead dimension and finish uncontrolled in L1
- ⁽³⁾ Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- $^{\left(4\right) }$ Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. 10.51 0.414 10.11 0.398 3,6 Е E1 6.86 8.89 0.270 0.350 6 E2 0.76 0.030 7 --2.41 2.67 0.095 0.105 е 0.208 e1 4.88 5.28 0.192 H1 6.09 6.48 0.240 0.255 6,7 13.52 14.02 0.532 0.552 L L1 3.32 3.82 0.131 0.150 2 ØΡ 3.54 3.73 0.139 0.147 2.60 0.102 Q 3.00 0.118 90° to 93° 90° to 93° θ

Conforms to JEDEC outline TO-220AB

- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.