



**Features** 

Low On-Resistance Low Threshold Fast Switching Speed Low Gate Drive

**Mechanical Data** 

Case: MSOP-8

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**DUAL 20V N-CHANNEL ENHANCEMENT MODE MOSFET** 

Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2) Halogen and Antimony Free. "Green" Device (Note 3) Qualified to AEC-Q101 Standards for High Reliability

Case Material: Molded Plastic, "Green" Molding Compound.

UL Flammability Classification Rating 94V-0 Moisture Sensitivity: Level 1 per J-STD-020

Terminals: Matte Tin Finish (9) Weight: 0.0277 grams (Approximate)

# **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>ds(on)</sub>	Package	I <sub>D</sub> T <sub>A</sub> = +25°C (Notes 5 & 6)	
20V	$130m\Omega @ V_{GS} = 4.5V$	MSOP-8	2.5A	
200	150m $\Omega$ @ V <sub>GS</sub> = 2.7V	WISOP-8	2.3A	

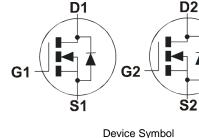
# Description

This MOSFET is designed to minimize the on-state resistance  $(R_{DS(ON)})$ , yet maintain superior switching performance, making it ideal for high efficiency power management applications.

# Applications

- DC-DC Converters
- Power Management functions
- Motor Control
- Disconnect Switches





S1 Θ ω 7 D1 G1 2 7 D1 S2 [ ø ] D2 Э G2[ ഹ ] D2 4

Top View

Pin-Out

Top View

# Ordering Information (Note 4)

Product	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
ZXMD63N02XTA	ZXM63N02	7	12	1,000
ZXMD63N02XTC	ZXM63N02	13	12	4,000

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

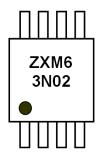
2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.

3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

# **Marking Information**

Notes:



ZXM63N02 = Product type Marking Code



#### Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic					Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	20	V			
Gate-Source Voltage	V <sub>GSS</sub>	±12	V			
Continuous Drain Current	Steady State	$@V_{GS} = 10V; T_A = +2$ $@V_{GS} = 10V; T_A = +7$ $@V_{GS} = 10V; T_A = +7$	70°C (Notes 5 & 6)	ID	2.5 1.9 0.78	A
Pulsed Drain Current			(Notes 6 & 7)	I <sub>DM</sub>	19	А
Continuous Source Current (Body Diode)			(Notes 5 & 6)	ls	1.5	А
Pulsed Source Current (Body Diode)			(Notes 6 & 7)	I <sub>SM</sub>	19	А

# **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
	(Notes 6 & 8)		0.87	
Power Dissipation	(Notes 5 & 6)	PD	1.25	W
	(Notes 8 & 9)		1.04	
	(Notes 6 & 8)		143	
Thermal Resistance, Junction to Ambient	(Notes 5 & 6)	R <sub>0JA</sub>	100	°C/W
	(Notes 8 & 9)		120	
Thermal Resistance, Junction to Leads	(Note 10)	R <sub>0JL</sub>	84.9	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

5. For a device surface mounted on FR4 PCB measured at t  $\leq$  10 sec.

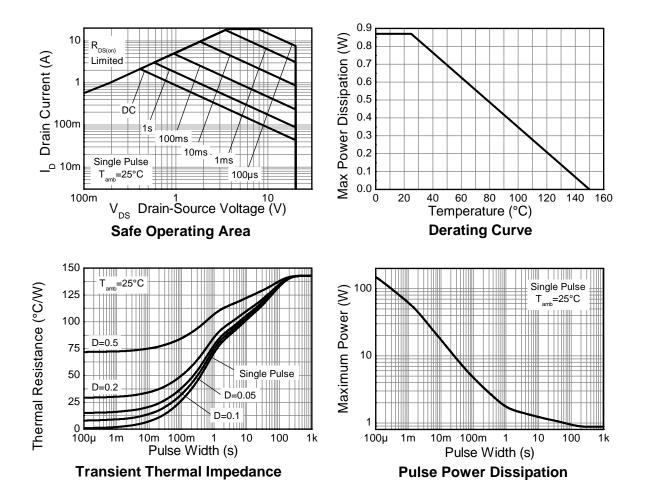
6. For device with one active die.

Notes:

For device with one active die.
Repetitive rating – 25mm x 25mm FR4 PCB, D = 0.02, pulse width 300µs – pulse width limited by maximum junction temperature.
For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
For device with two active die running at equal power.
Thermal resistance from junction to solder-point (at the end of the drain lead).



# **Thermal Characteristics**





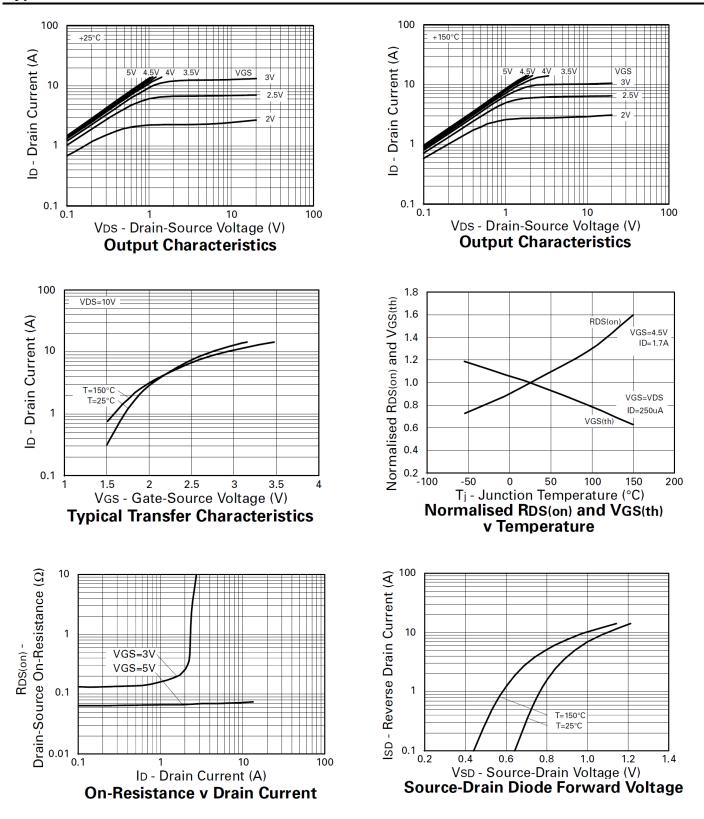
# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

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	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS					.,		
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	—	—	1.0	μA	$V_{DS} = 20V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>		—	100	nA	$V_{GS} = \pm 12V$ , $V_{DS} = 0V$	
ON CHARACTERISTICS							
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.7	—	3	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	
Static Drain-Source On-Resistance (Note 11)	Р		65	130	mΩ	$V_{GS} = 4.5V, I_D = 1.7A$	
Static Drain-Source On-Resistance (Note 11)	R <sub>DS(ON)</sub>	_	90	150		$V_{GS} = 2.7V, I_D = 0.85A$	
Forward Transconductance (Notes 11 & 13)	<b>g</b> fs	2.6	—	—	S	$V_{DS} = 10V, I_D = 0.85A$	
Diodes Forward Voltage (Note 11)	V <sub>SD</sub>	_	0.85	0.95	V	$T_J = +25^{\circ}C, I_S = 1.7A, V_{GS} = 0V$	
DYNAMIC CHARACTERISTICS							
Input Capacitance (Notes 12 & 13)	Ciss	—	350	700		$V_{DS} = 15V, V_{GS} = 0V,$ f = 1.0MHz	
Output Capacitance (Notes 12 & 13)	C <sub>oss</sub>	_	120	250	pF		
Reverse Transfer Capacitance (Notes 12 & 13)	C <sub>rss</sub>	_	50	100			
Gate Resistance (Notes 12 & 13)	Rg	—	3.8	7.6	Ω	$f = 1MHz$ , $V_{GS} = 0V$ , $V_{DS} = 0V$	
Total Gate Charge (Notes 12 & 13)	Qg	_	4.5	6			
Gate-Source Charge (Notes 12 & 13)	Q <sub>gs</sub>	_	0.5	0.65	nC	V <sub>GS</sub> = 4.5V, V <sub>DS</sub> = 16V, I <sub>D</sub> = 1.7A	
Gate-Drain Charge (Notes 12 & 13)	Q <sub>gd</sub>	_	2	2.5			
Reverse Recovery Time (Note 13)	t <sub>rr</sub>	—	15	30	nS	T <sub>.1</sub> = +25°C, I <sub>F</sub> = 1.7A,	
Reverse Recovery Charge (Note 13)	Q <sub>rr</sub>	—	5.9	—	nC	di/dt = 100A/µs	
Turn-On Delay Time (Notes 12 & 13)	t <sub>D(on)</sub>	—	3.4	—			
Turn-On Rise Time (Notes 12 & 13)	tr	_	8.1	—	nS	$V_{DD} = 10V, I_D = 1.7A,$	
Turn-Off Delay Time (Notes 12 & 13)	t <sub>D(off)</sub>	—	13.5	—	115	$R_G = 6\Omega, R_D = 5.7\Omega,$	
Turn-Off Fall Time (Notes 12 & 13)	t <sub>f</sub>		9.1		1		

11. Measured under pulsed conditions. Pulse width  $\leq$  300µs; duty cycle  $\leq$ 2%. 12. Switching characteristics are independent of operating junction temperature. 13. For design aid only, not subject to production testing. Notes:

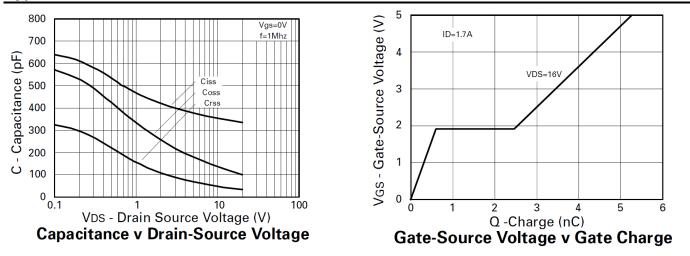


#### **Typical Characteristics**

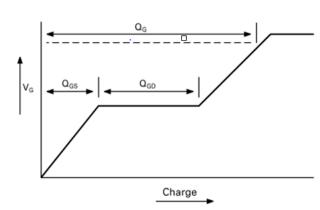




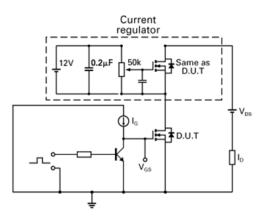
#### Typical Characteristics (Continued)



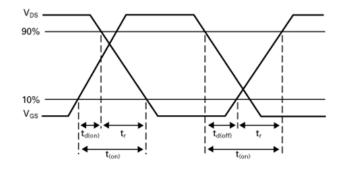
**Test Circuits** 



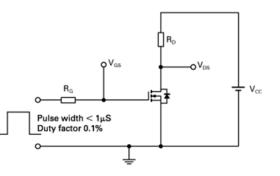
Basic gate charge waveform



Gate charge test circuit



Switching time waveforms

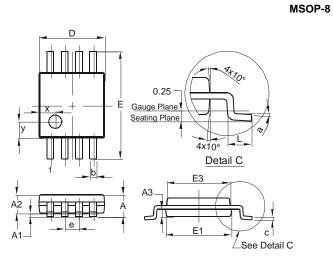


# Switching time test circuit



# **Package Outline Dimensions**

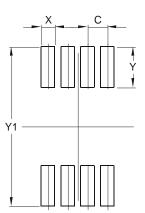
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



MSOP-8						
Dim	Min	Max	Тур			
Α	I	1.10	-			
A1	0.05	0.15	0.10			
A2	0.75	0.95	0.86			
A3	0.29	0.49	0.39			
b	0.22	0.38	0.30			
С	0.08	0.23	0.15			
D	2.90	3.10	3.00			
Ε	4.70	5.10	4.90			
E1	2.90	3.10	3.00			
E3	2.85	3.05	2.95			
е	-	-	0.65			
L	0.40	0.80	0.60			
а	0°	8°	4°			
Х	-	-	0.750			
У	-	-	0.750			
	All Dimensions in mm					

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.650
Х	0.450
Y	1.350
Y1	5.300

MSOP-8



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